

AUG - 4 2004

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Date: August 4, 2004
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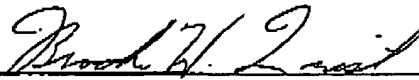
PAGE 1/13 * RCVD AT 8/4/2004 2:49:19 PM [Eastern Daylight Time] * SVR:USPTO-EFAX-2/2 * DNIS:7467239 * CSID:3107128383 * DURATION (mm-ss):05-00

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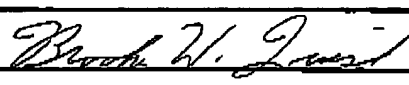
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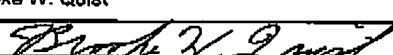
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TRANSMITTAL FORM <small>(to be used for all correspondence after initial filing)</small>	Application Number	10/032,949	
	Filing Date	December 26, 2001	
	First Named Inventor	Naoki Mukaida	
	Art Unit	2188	
	Examiner Name	Inoa, Midys	
Total Number of Pages in This Submission		Attorney Docket Number	10416/18

ENCLOSURES (check all that apply)		
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In re Application of: Naoki Mukaida
Serial No.: 10/032,949 Examiner: Inoa, Midys
Filed: December 26, 2001 Group Art Unit: 2188
Title: MEMORY CONTROLLER, FLASH MEMORY SYSTEM
HAVING MEMORY CONTROLLER AND METHOD FOR
CONTROLLING FLASH MEMORY DEVICE

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE TO OFFICE ACTION

Sir:

This amendment is responsive to the Office action of May 7, 2004, and is timely filed.

INTRODUCTORY COMMENTS

Claims 1-20 are pending in the present application. Claims 1-20 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Estakhri et al. (U.S. Patent No. 5,835,935) in view of Konishi et al. (U.S. Patent No. 5,579,502). Claims 1, 10, and 20 have been amended. No new claims have been added. No claims have been deleted. Applicant respectfully contends that the differences between the claimed invention and the prior art are such that the claimed invention is patentably distinct over the prior art.